

SEARCHED		INDEXED
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CLASS	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

G.I.P.E.		PATENT DATE
KS 3/28/03 SCANNED		2003-03-28 A.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/733036	D F	372	109	2881	Leung

Kenji Yamauchi

TITLE Semiconductor laser diode chip and its positoning and mouting method

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date) _____			ISSUE FEE	
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